

ONIX-FMCP-SFP28-X4

FMC+ HPC Board with 4 x SFP28 Transceivers

Product Overview

This document provides information regarding the ONIX-FMCP-SFP28-X4 board. The board complies with the FMC HPC, VITA 57.4, and VITA 57.1 standards.

Block Diagram

The ONIX-FMCP-SFP28-X4 block diagram shown in Figure 1.

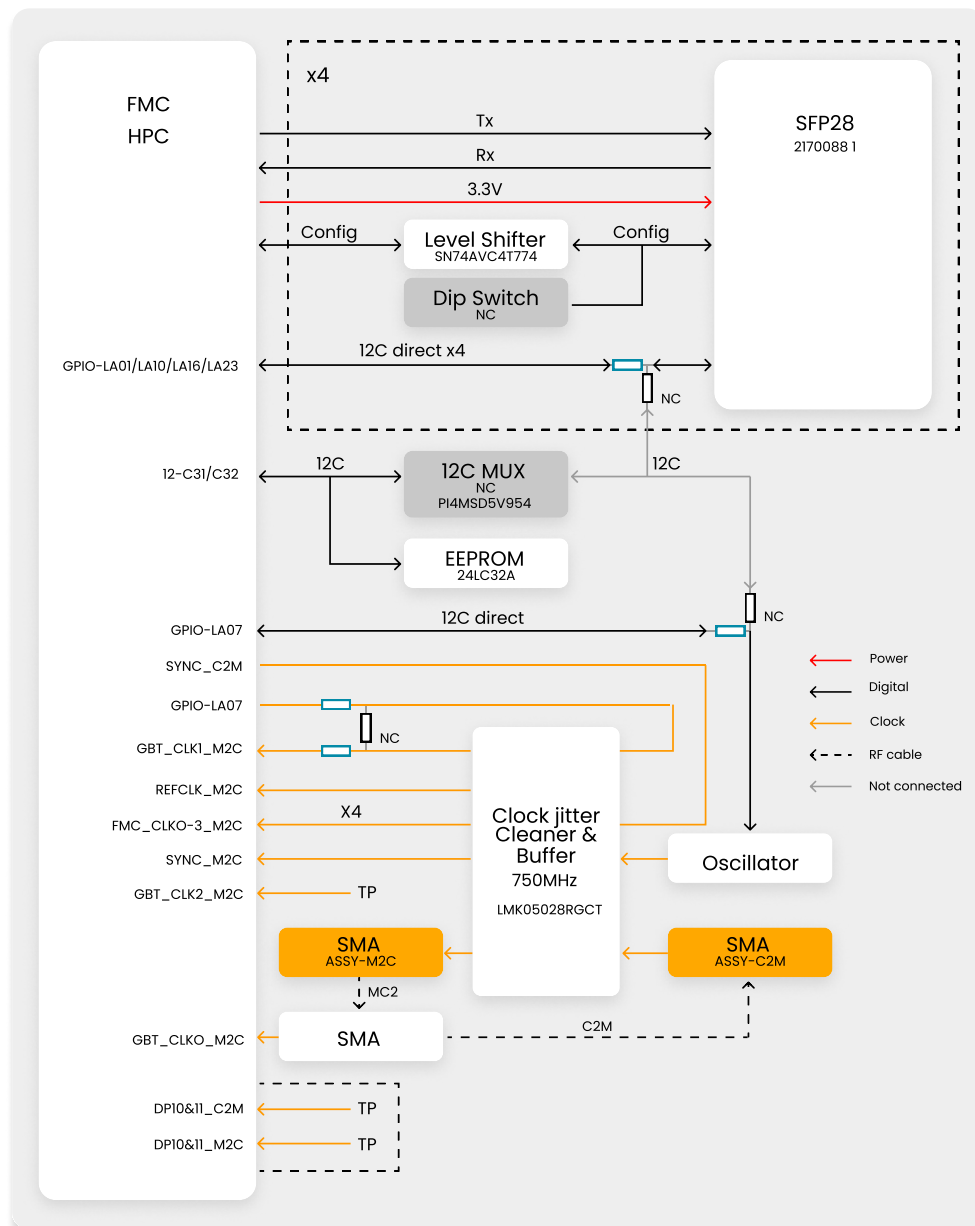


Figure 1 - ONIX-FMCP-SFP28-X4 Block Diagram

Main Features

- VITA 57.4 FMC HPC connector.
- Compatible with the VITA 57.1 FMC HPC.
- 4 x SFP28 25 Gbps ports.
- Low jitter programmable clock generator.
- 32Kbit I2C Serial EEPROM.
- Status LEDs.
- SI Simulated for High-Speed Performance.
- High-Performance PCB Materials (Isola I-TERA MT40).

Transceivers:

- 4 x SFP28 connectors.
- Form factor complying with SFP+ connectors.
- SFP Signals are connected to FMC 'DP' pins.
- SI simulations performed for the High-speed signals.
- [VITA 57.4 FMC HPC connector](#).
- FMC HPC Connector can connect to both FMC
- and FMC+ Carrier connectors.

Low jitter programmable clock generator:

- Maximum frequency of 800MHz.
- Maximum Jitter output 100ps.
- On-board 100MHz LVDS oscillator.
- External LVDS or CMOS clock input up to 800MHz.
- Internal BIDIR_CLK loop throw jitter attenuator.
- Clock is switched between internal CLK/On-board OSC/External CLK.
- I2C programmable.

LEDs:

- C2M_PG.
- Jitter Attenuator status.
- Tx Fault / Rx Loss Indicators.

Ordering Information

Part Number	Description	Price
ONIX-FMCP-SFP28-X4	FMC+ HPC Board with 4 x SFP28 Transceivers	1,100\$

Related Products

Part Number	Description	Price
ONIX-VU440-2	ASIC Prototyping Board based on the AMD XCVU440-2 FPGA	30,000\$

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